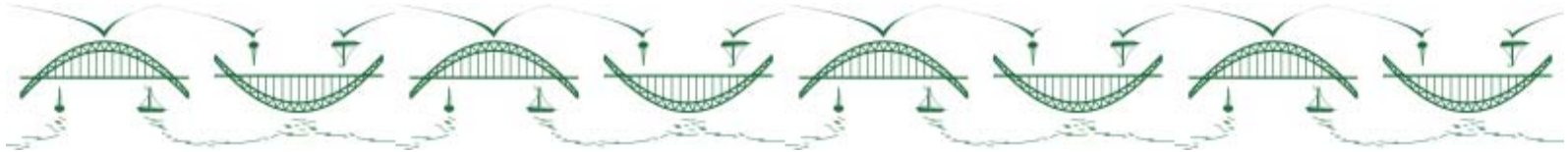




ASync 2008 Best Paper Award



Procedure



- Top-ranked papers from review process.
- PC-members invited to nominate additional papers.
- Best paper award selection committee proposed to, and accepted by, conference program chairs.
- 6 papers considered.

Committee members



ASYNC'08 Best paper award selection committee:

- [Josep Carmona](#), Universitat Politecnica de Catalunya, Spain.
- [Douglas Edwards](#), University of Manchester, UK.
- [Ran Ginosar](#), Technion, Israel.
- [Mark Greenstreet](#), University of British Columbia, Canada.
- [Chris J. Myers](#), University of Utah, USA.
- [Jens Sparsø](#), Technical University of Denmark.

Procedure



- Round 1: Committee discussed the candidate papers and selected the 3 top papers. Consensus among all committee members.
- Round 2: Further discussions based on camera ready papers. Voting: "Indicate your #1 and #2"

Result



	Committee members					
Paper A	1	1	1	1	1	1
Paper B	2	2		2		2
Paper C			2		2	

- We have one best paper and two runners up

ASYNCH'08 Best Paper



"Automatic Power Regulation based on an Asynchronous Activity Detection and its Application to ANOC Node Leakage Reduction". Yvain Thonnart, Edith Beigné, Alexandre Valentian, Pascal Vivet, CEA-LETI, FRANCE

- This is the first paper to attempt to address leakage power management in a self-timed NoC, and it shows promising results for the chosen application.
- Addresses a very important issue in deep sub-micron design: the leakage power and suggest
- Two strong positives are that the circuit (a) is simple and (b) was fabricated and measured.
- Authors not only provide ideas and a model (ANoC), but also give a fabricated chip to validate the claims. The work is very significant.



ASYN'C'08 Best paper award