

ASYNCHRONOUS 2008 Call for Papers

7th - 11th April 2008 – Newcastle upon Tyne, UK



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The 14th IEEE International Symposium on Asynchronous Circuits and Systems

The International Symposium on Asynchronous Circuits and Systems provides a high-quality forum for scientists and engineers to present their latest research findings. Authors are invited to submit full papers on all aspects of asynchronous design. Topics of interest include, but are not limited to:

- Mixed synchronous/asynchronous architectures, interfaces, and circuits.
- Design, synthesis and Verification techniques for GALS systems.
- Synchronous-asynchronous interaction at different levels.
- High-speed/low-power asynchronous logic, memories, and interconnects.
- High-level design and synthesis of self-timed circuits.
- Physical design of unlocked logic and pipelines.
- Formal methods for correctness and performance analysis of asynchronous designs.
- Test, reliability, security, and radiation tolerance.
- CAD for asynchronous design and validation.
- Asynchronous System-on-Chip (SoC), System-in-Package (SiP).
- Novel asynchronous architectures.
- Asynchrony and latency tolerance in system-level design.
- Motivating case studies, comparisons, and applications.
- Embedded System Design with asynchronous architectures/implementations.
- Asynchronous design for manufacturing.

Papers must be submitted via the conference web site. Papers will be evaluated by the program committee and reviews will be based on scientific merit, innovation, relevance, and presentation. New idea papers are encouraged, and the program committee recognizes that such papers may contain less evaluation than papers in established areas. Accepted papers will be published in an IEEE proceedings and distributed at the symposium. Please check the symposium website for up-to-date information:

<http://async.org.uk/async2008/>

Important dates:

- Submission information:
 - Abstract registration: October 8, 2007
 - Full paper submission: October 15, 2007
- Notification of acceptance: December 12, 2007
- Final version due: January 18, 2008

Paper format: Abstract of up to 150 words, 10 pages or fewer, including figures and bibliography, single spaced, 10pt or larger font size, IEEE double-column conference format.